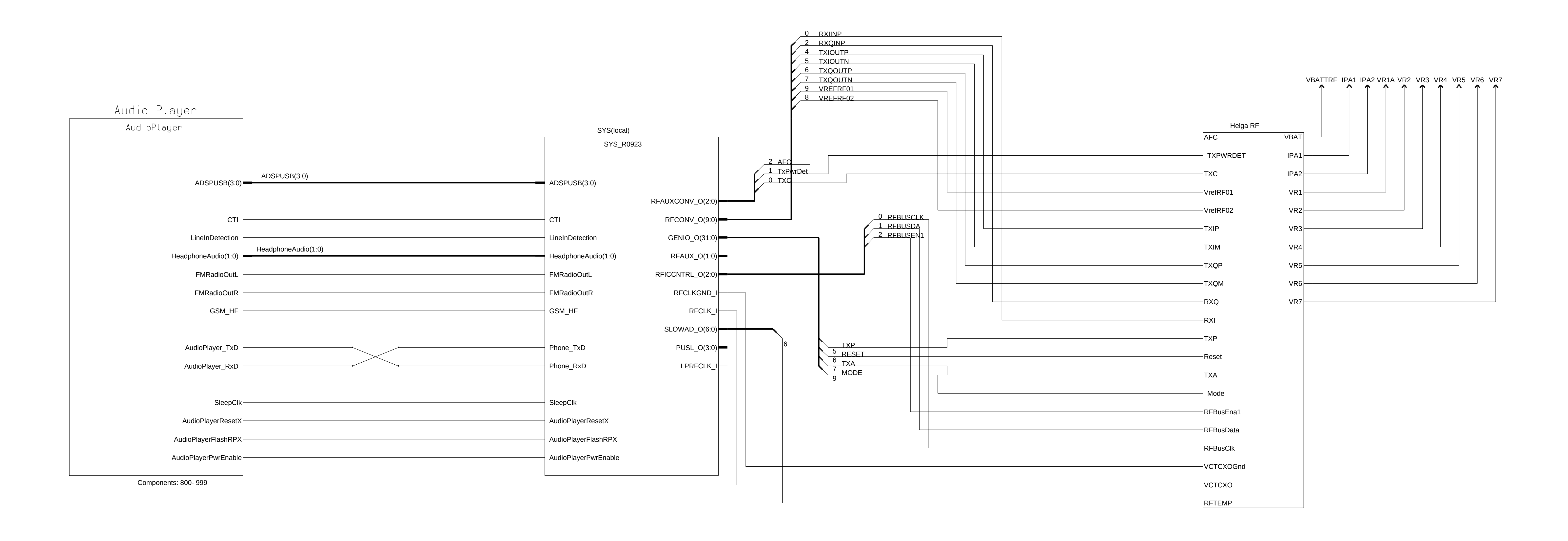
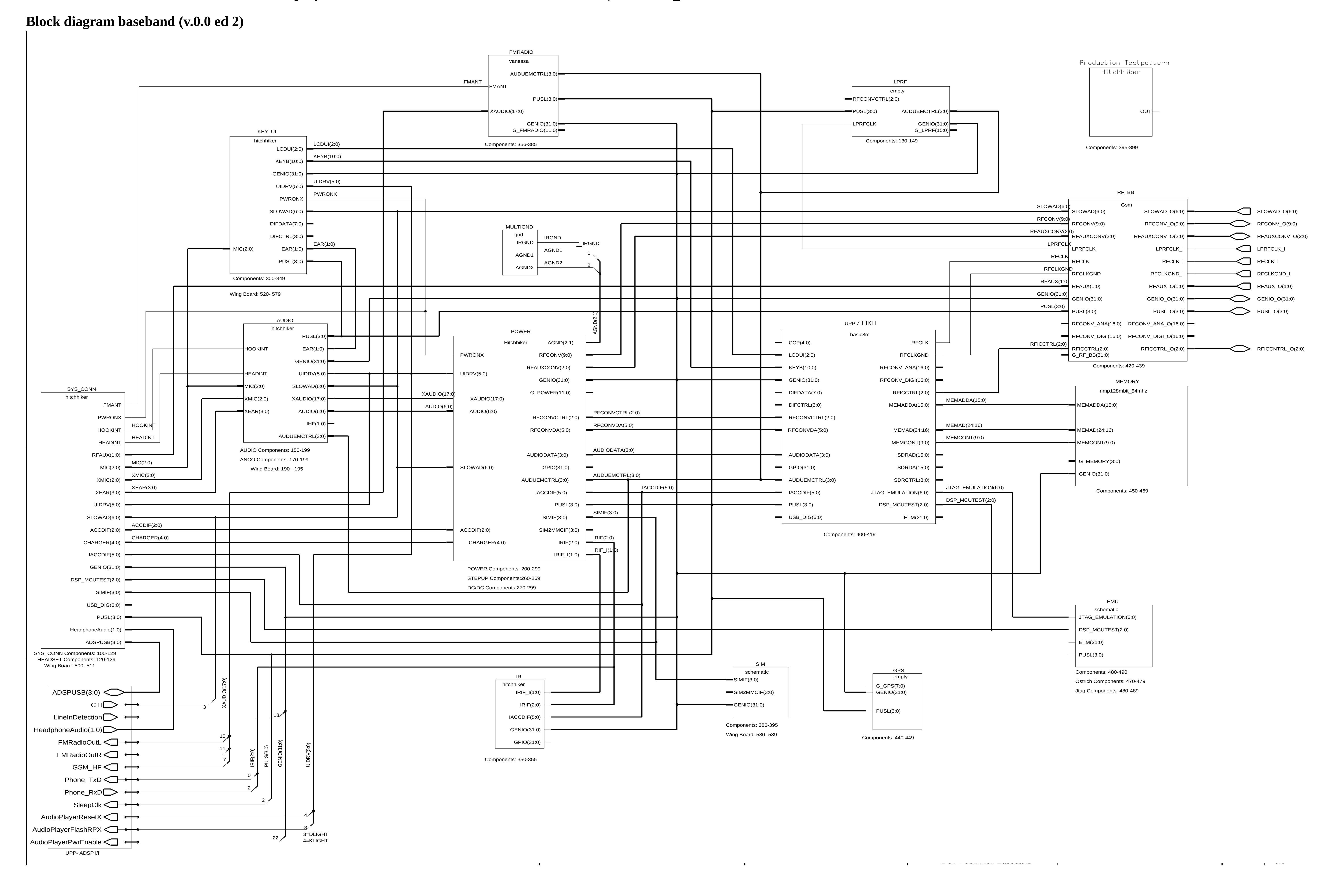
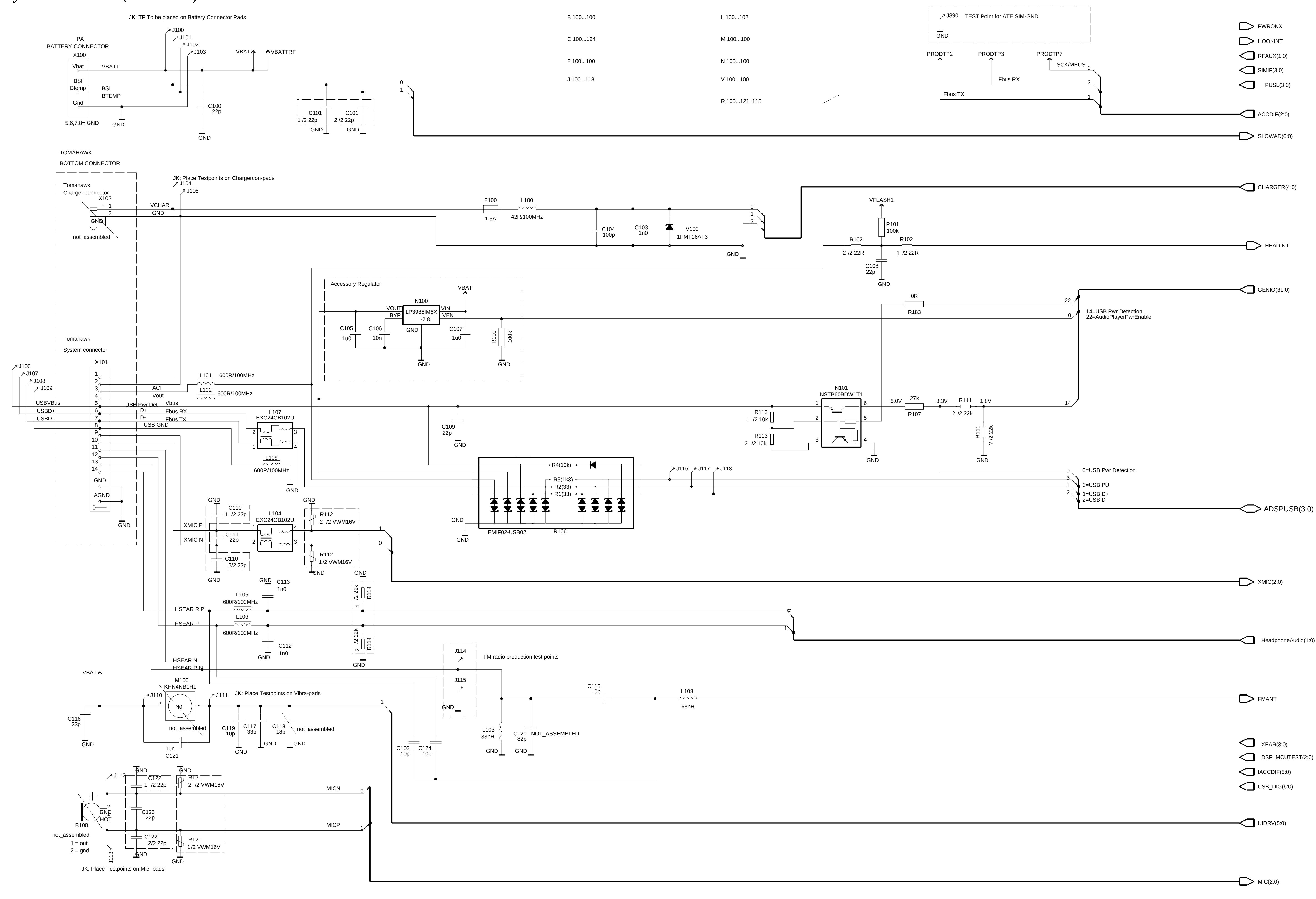
Block diagram EM11_10 (ver.0.0 ed.2)



Block diagram baseband (v.0.0 ed 2)



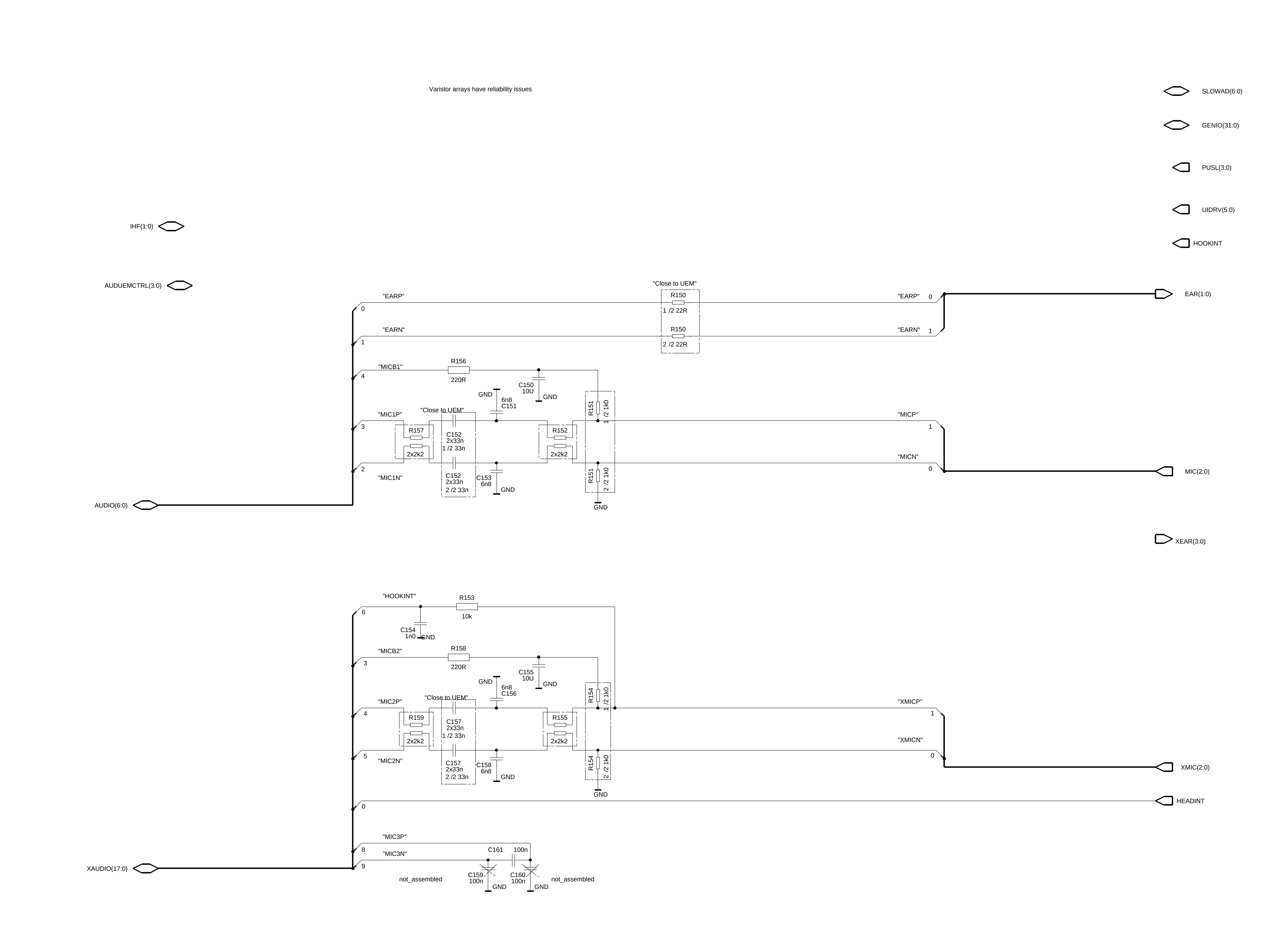
System Connector (ver. 0 ed. 2)



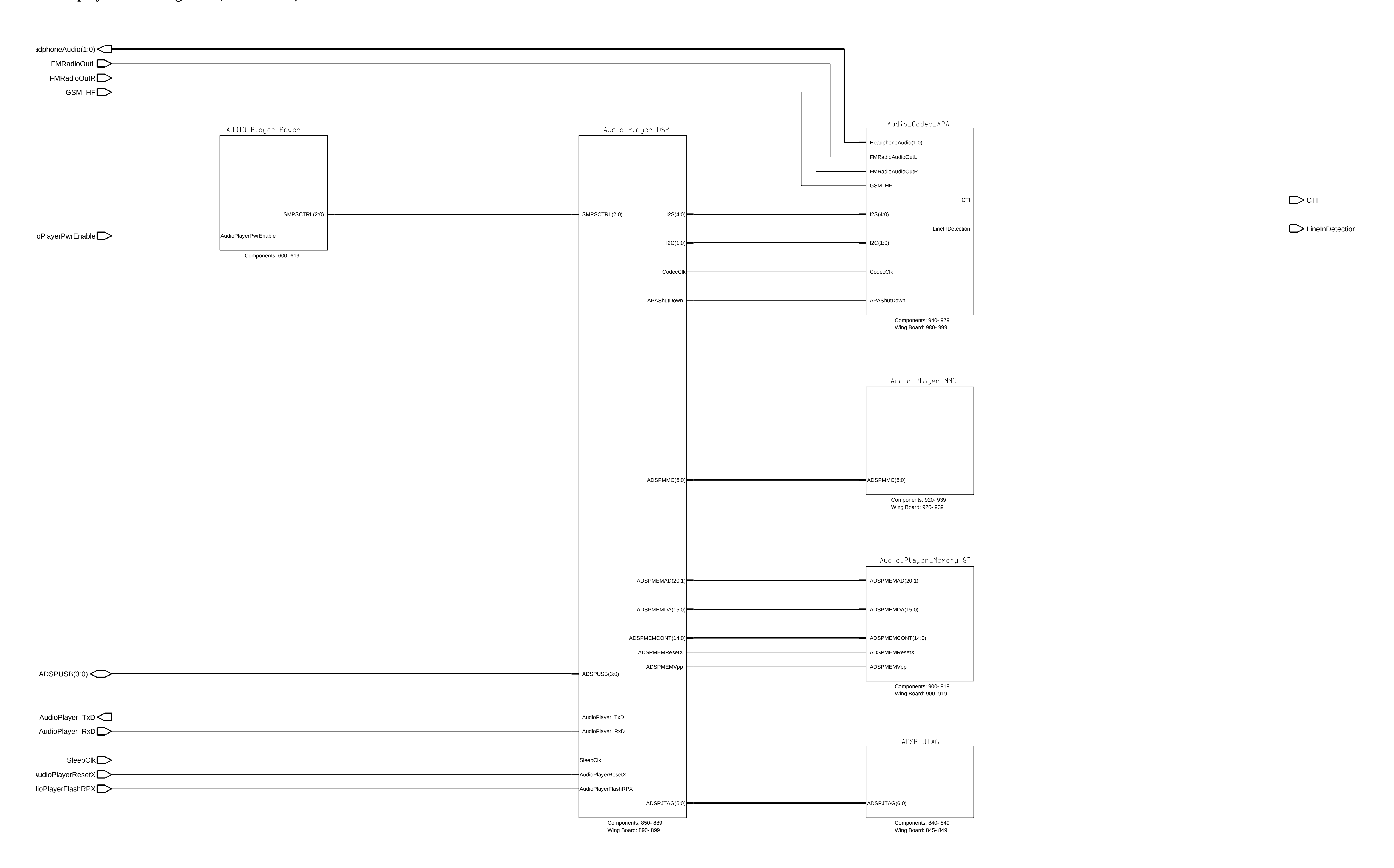


Key user Interface (ver.0.0 ed. 2)

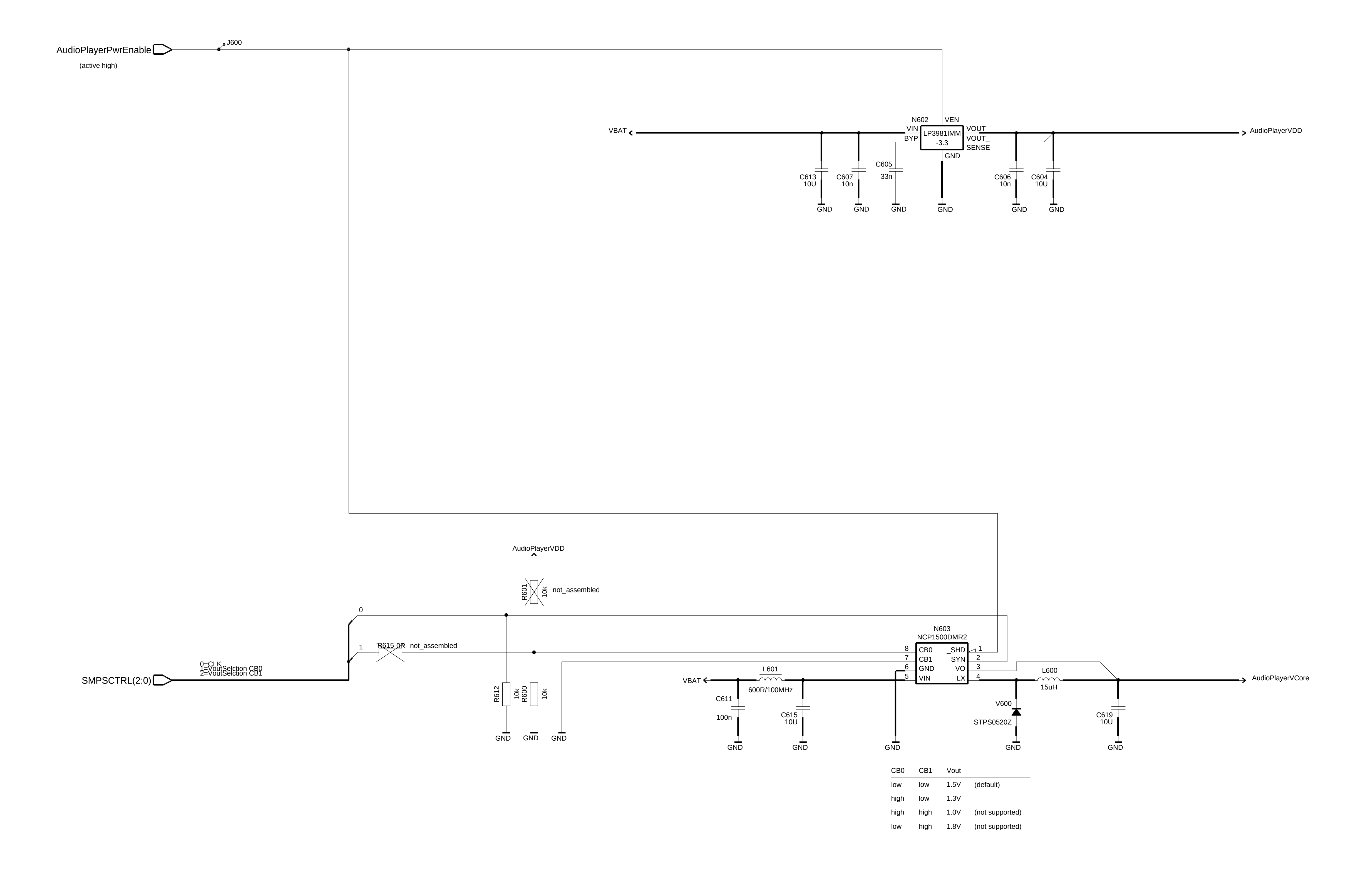
Audio (ver. 0.0 ed. 2)



Audio player block diagram (ver.0.0 ed. 2)



Audio Power (ver.0 ed. 2)



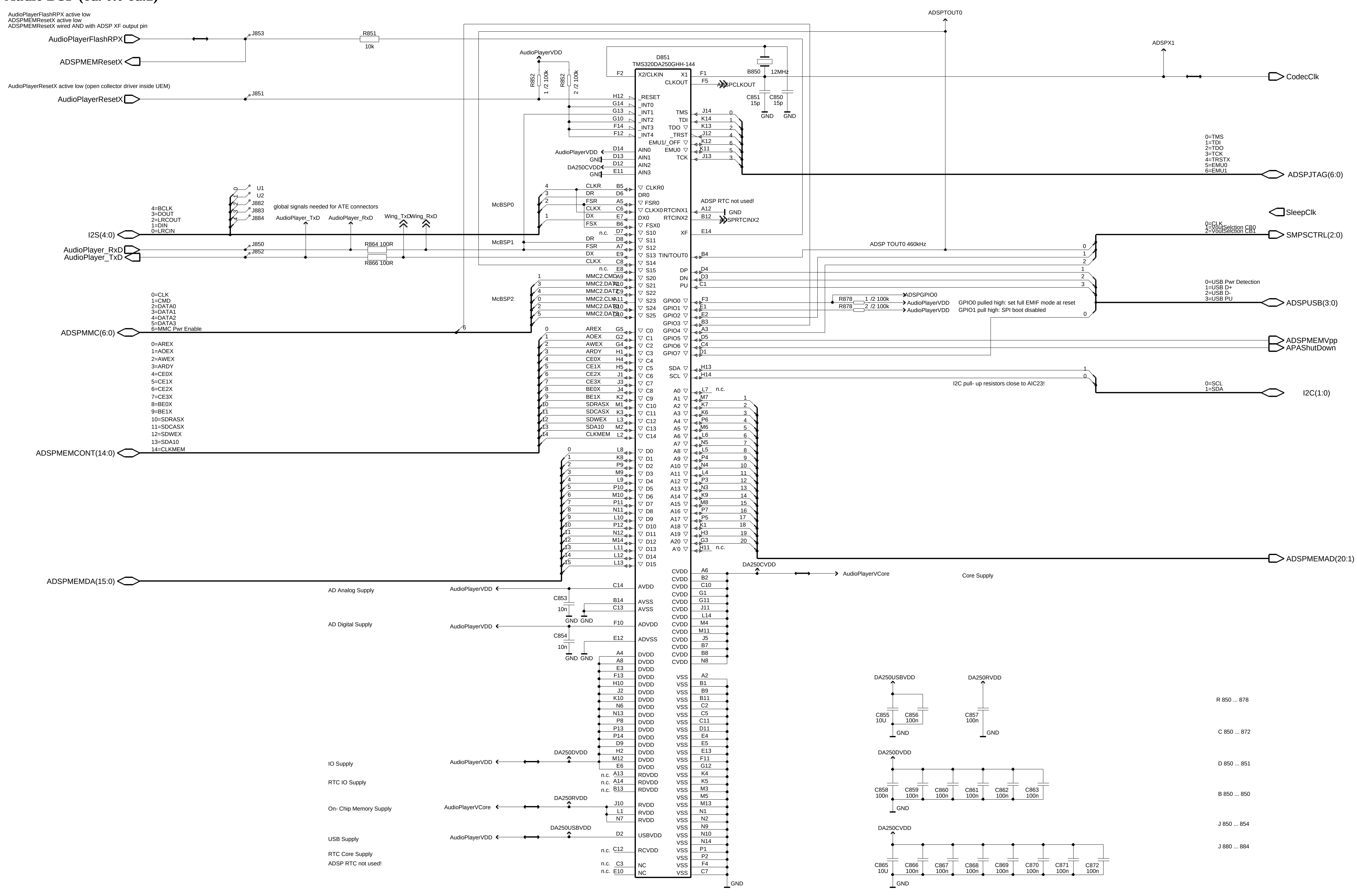
CB0	CB1	Vout	
low	low	1.5V	(default)
high	low	1.3V	
high	high	1.0V	(not supported)
low	high	1.8V	(not supported)

C 600 ... 619

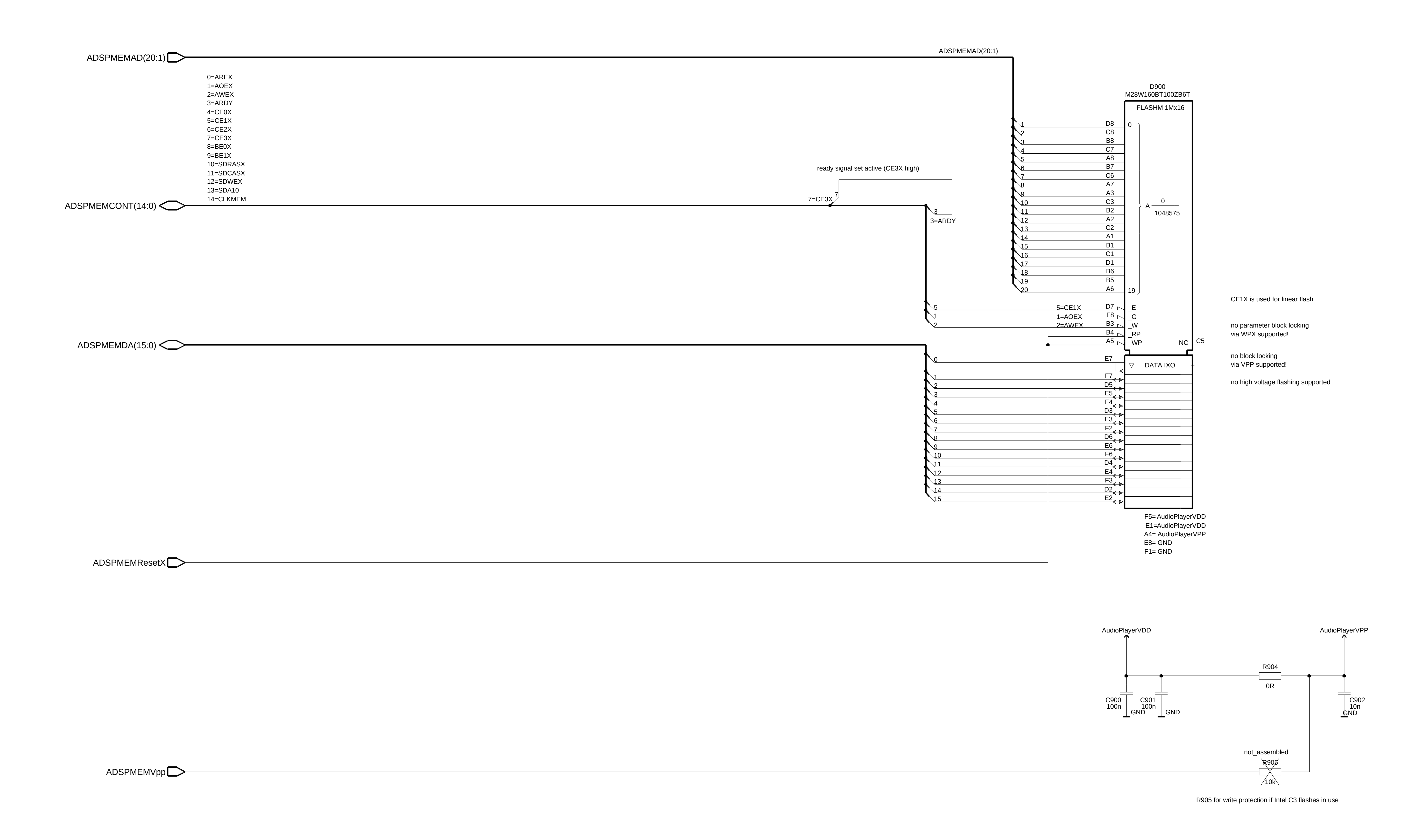
N 600 ... 603

User Interface

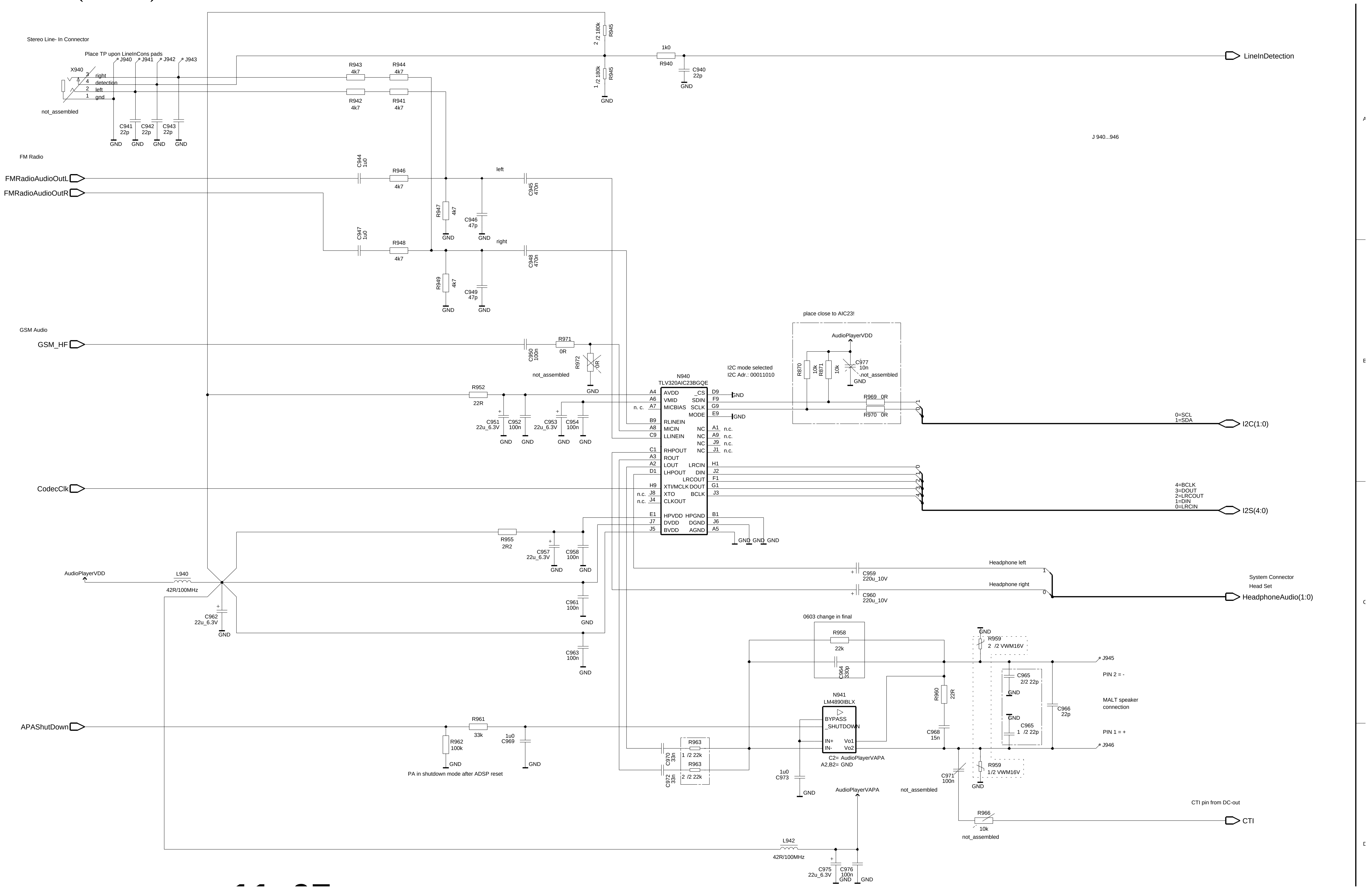
Audio DSP (ed. 0.0 ed.2)



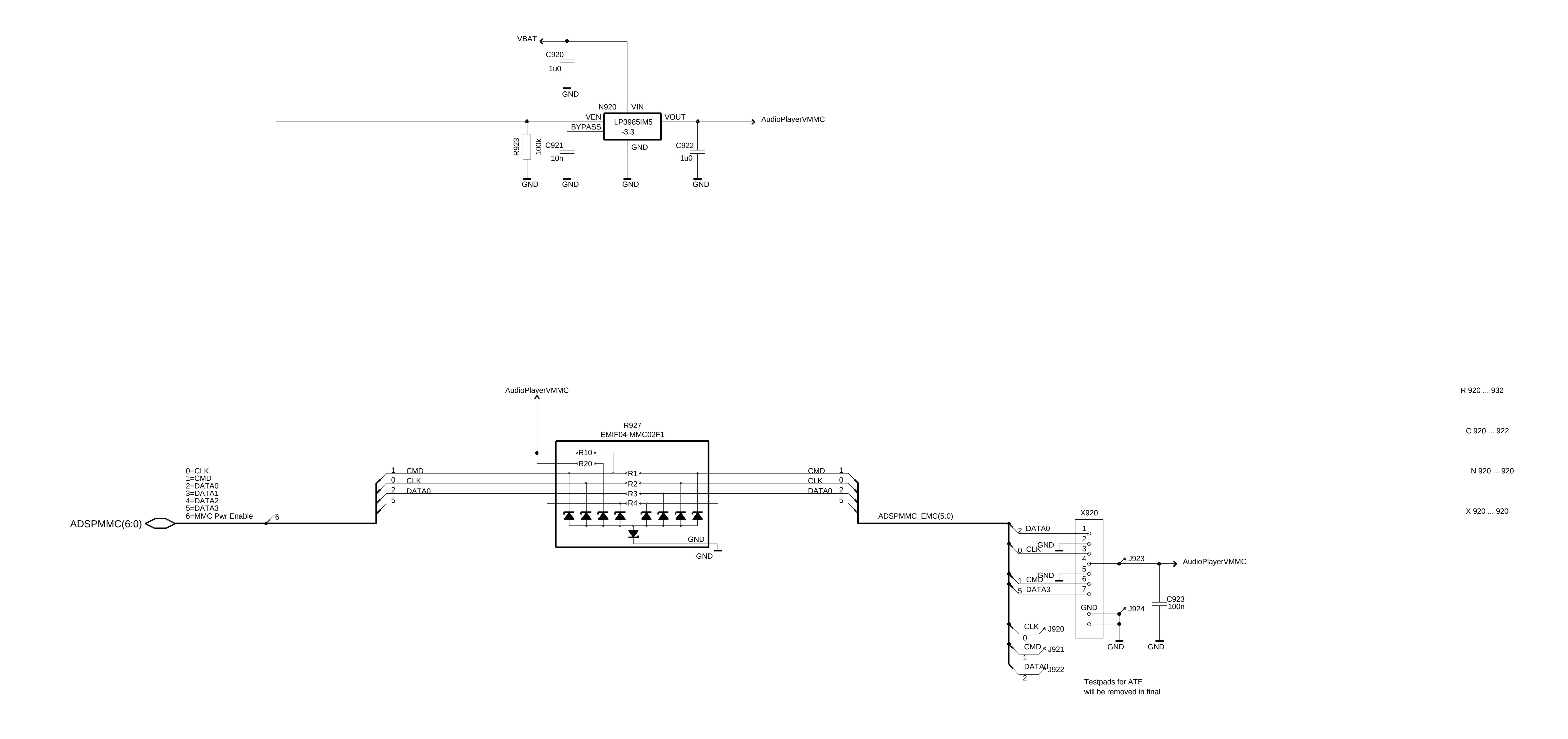
Memory ST (ver. 0 ed.2)



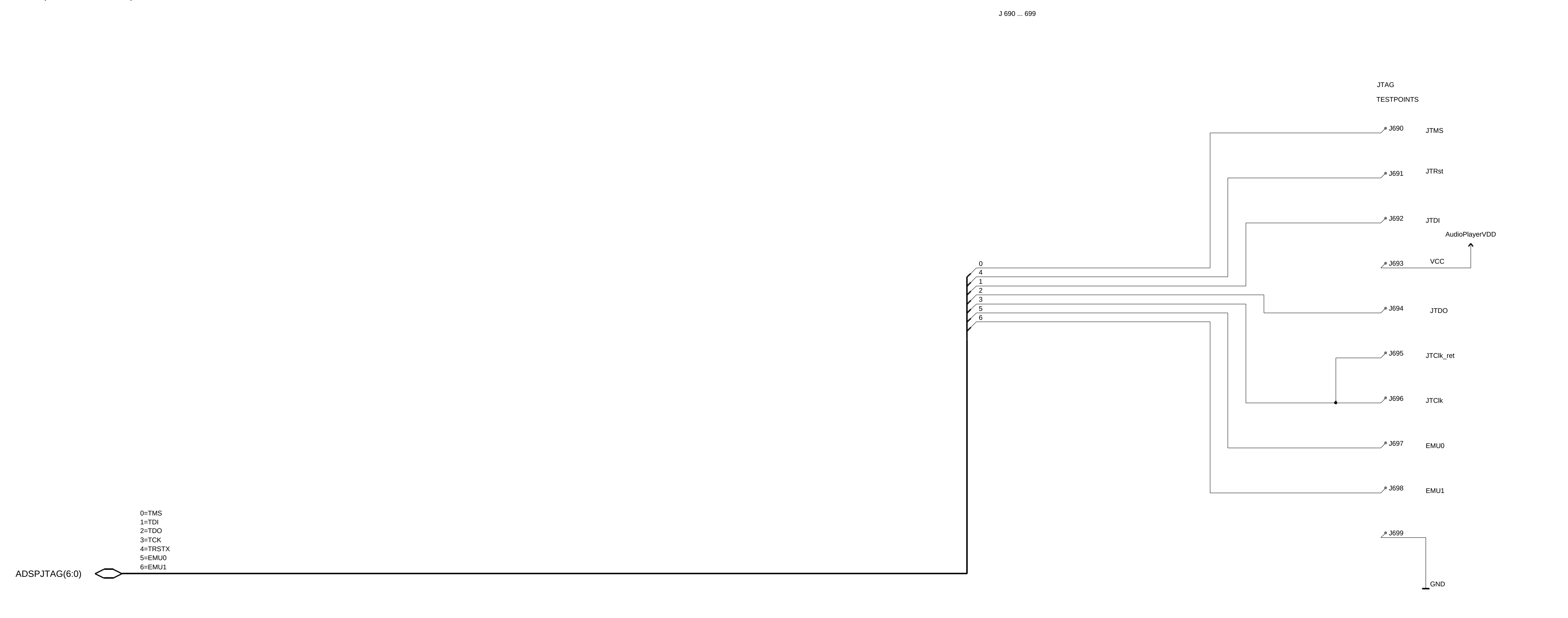
Audio Codec (ed. 0 ver. 2)



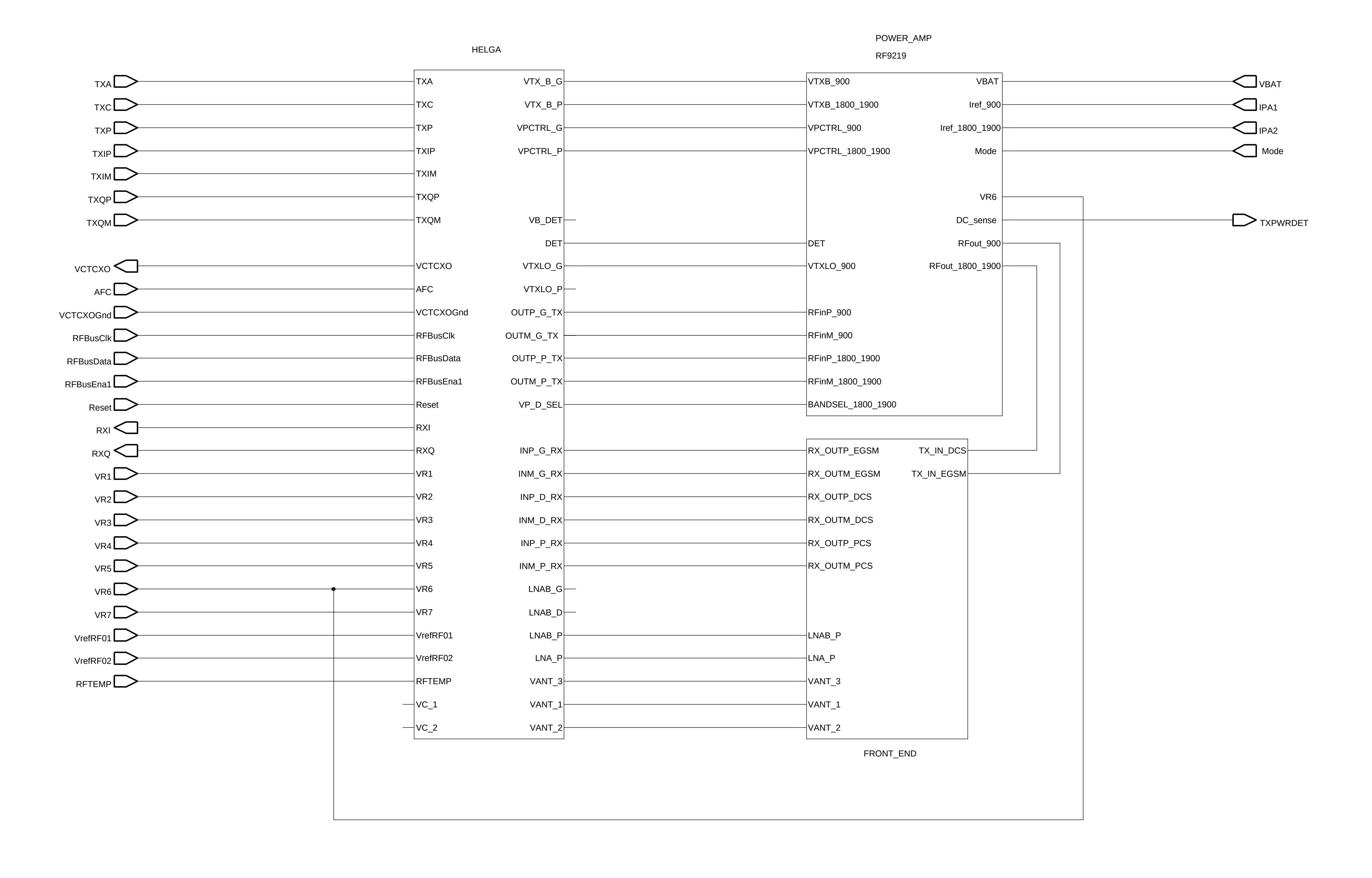
MMC (v.0 ed. 2)


VBAT
C920
1u0
GND
N920
VIN
VEN
VOUT
BYPASS
LP3985IM5
-3.3
AudioPlayerVMMC
R923
100k
C921
10n
GND
C922
1u0
GND
AudioPlayerVMMC
R927
EMIF04-MMC02F1
R10
R20
R1
R2
R3
R4
0=CLK
1=CMD
2=DATA0
3=DATA1
4=DATA2
5=DATA3
6=MMC Pwr Enable
ADSPMMC(6:0)
CMD
CLK
DATA0
ADSPMMC_EMC(5:0)
X920
DATA0
CLK
CMD
DATA3
J923
J924
C923
100n
J920
J921
J922
Testpads for ATE
will be removed in final
R 920 ... 932
C 920 ... 922
N 920 ... 920
X 920 ... 920

JTAG interface (ed. 0 ver. 2)



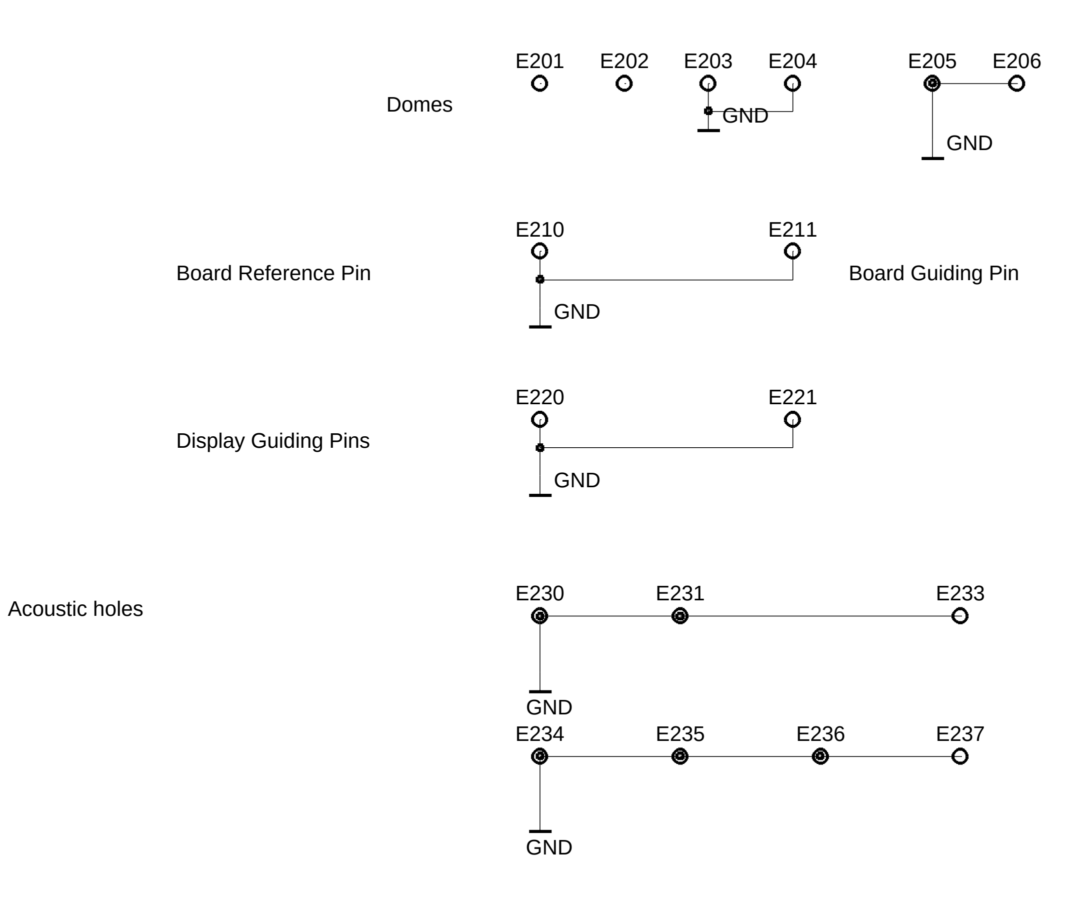
RF block diagram (ed.0.0 ver. 2)



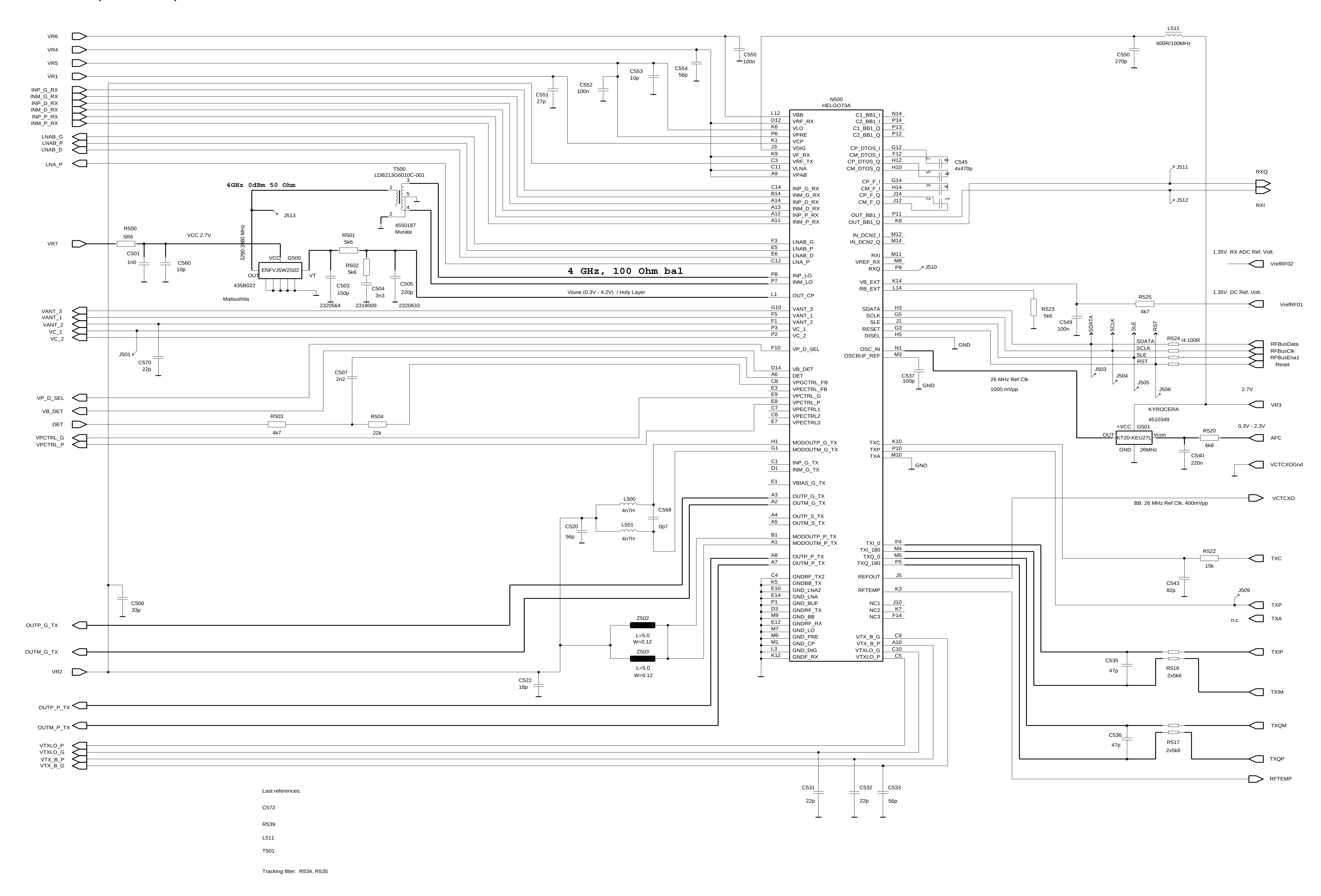
RF grounds (ed. 0 ver.2)

RF GLOBAL GND HOLES

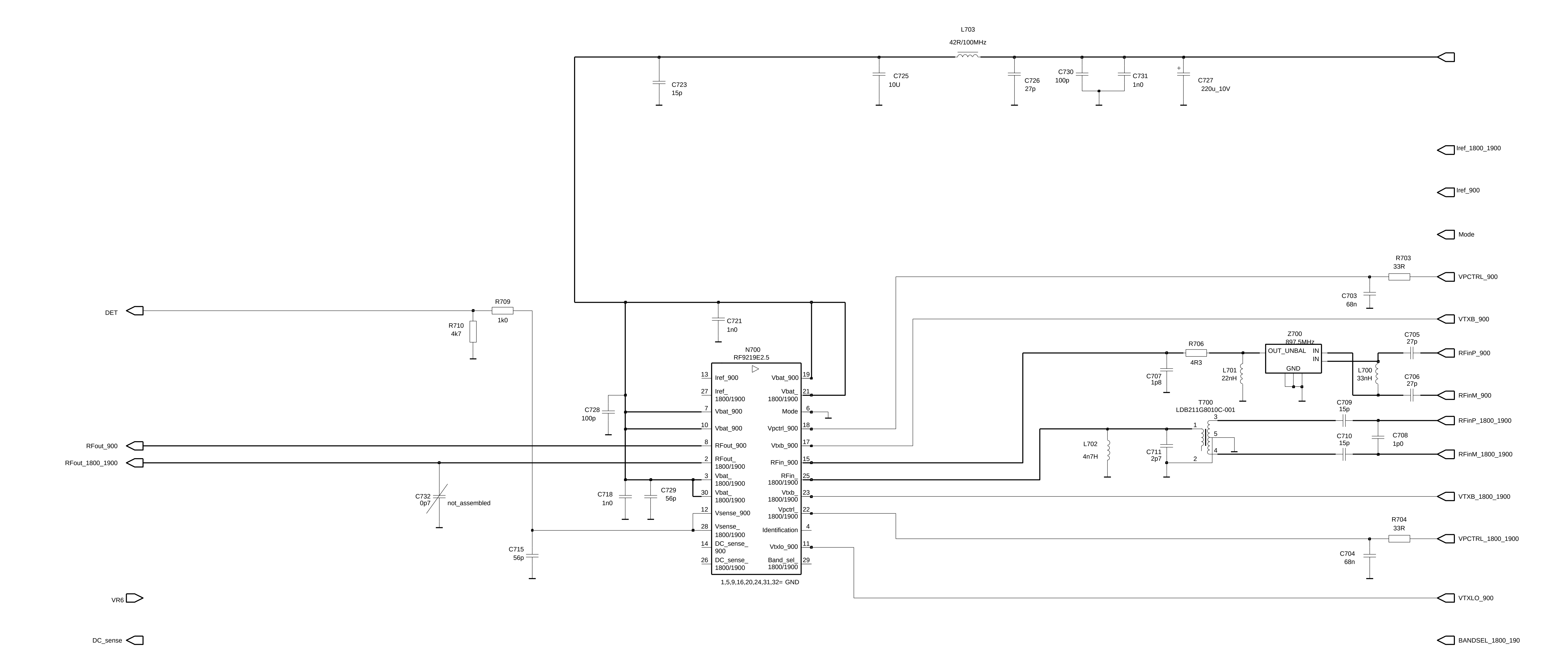
SYSTEM GLOBAL GND HOLES



HELGO ASIC (ver. 0.0 ed.2)



Power amplifier and detection (ver.2.0 ed 2)



Last references:

C732

R720

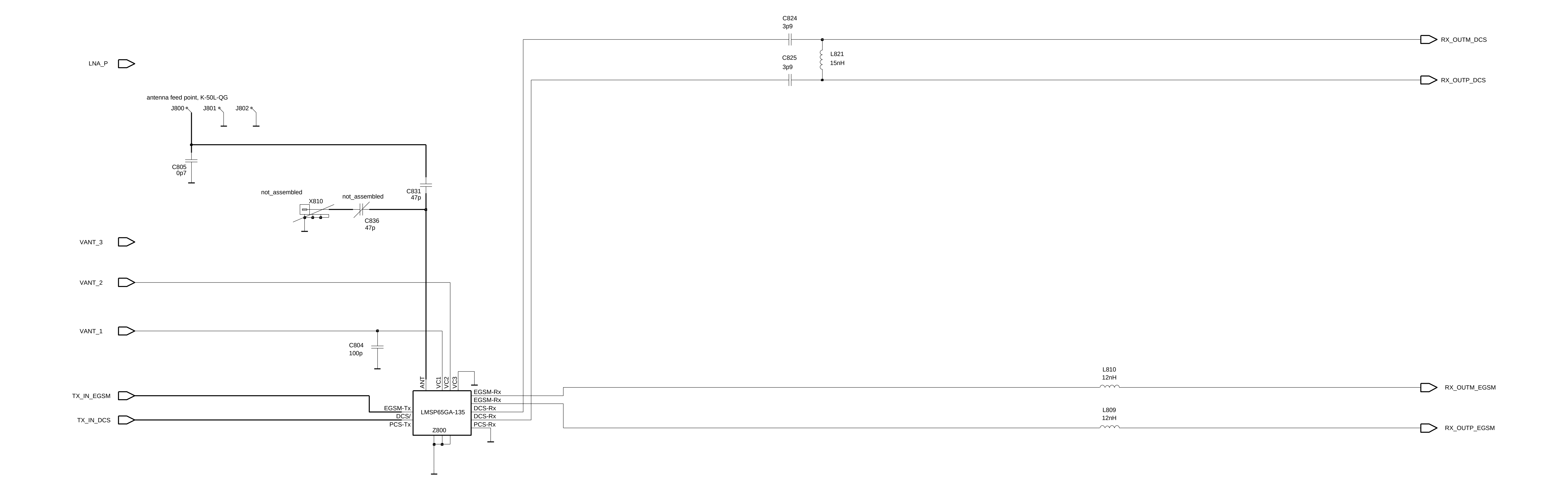
L704

RX front end and antenna swit (Ver. 2.0 ed. 2)

LNAB_P

RX_OUTM_PCS

RX_OUTP_PCS



Last references:

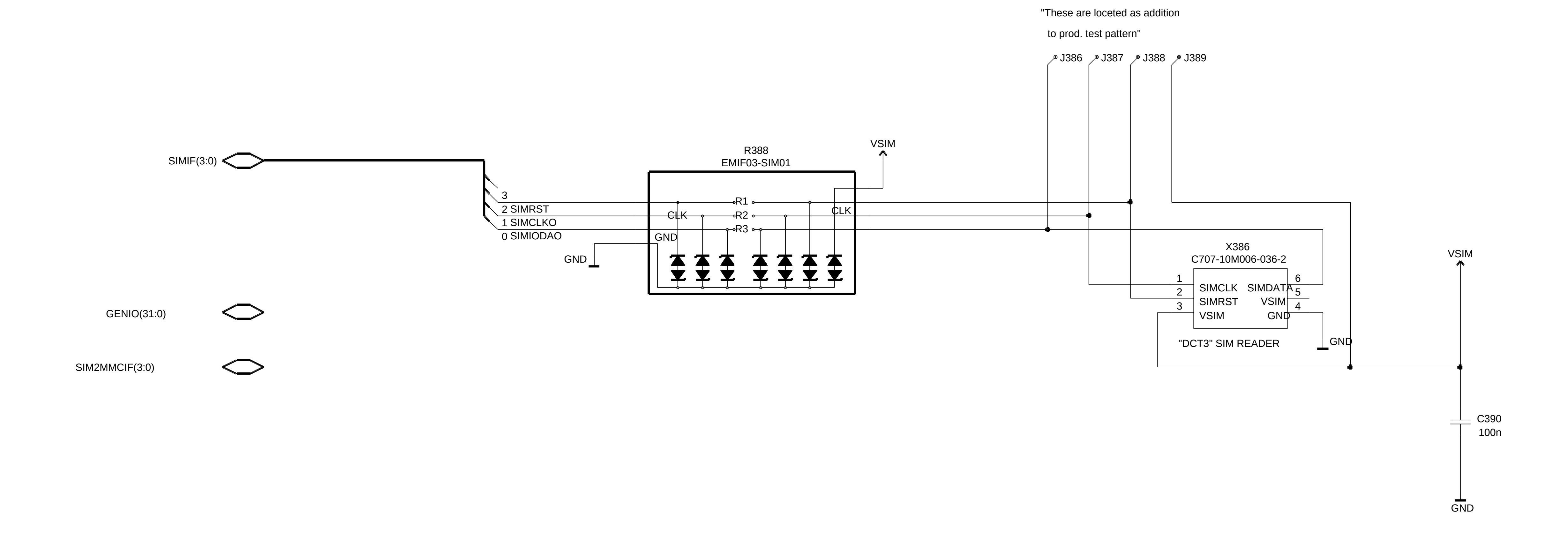
C835

R806

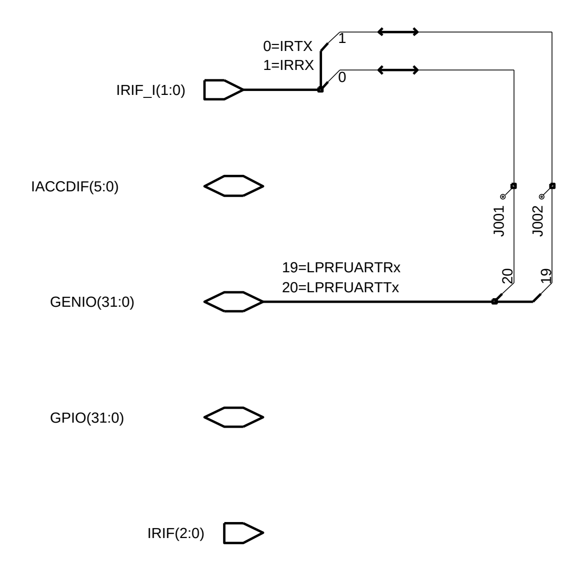
L825

Z806

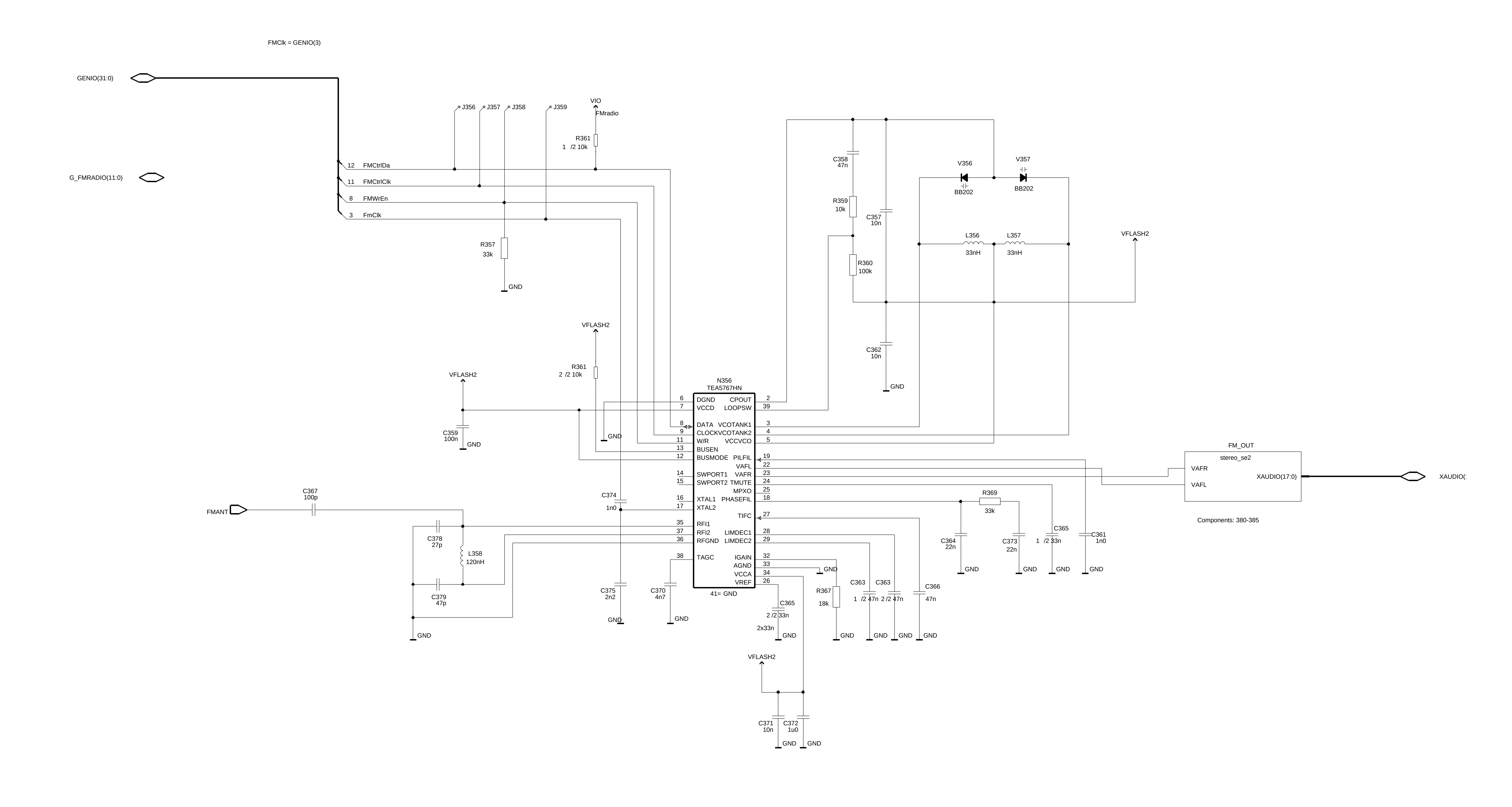
SIM reader (ver. 0.0 ed. 2)



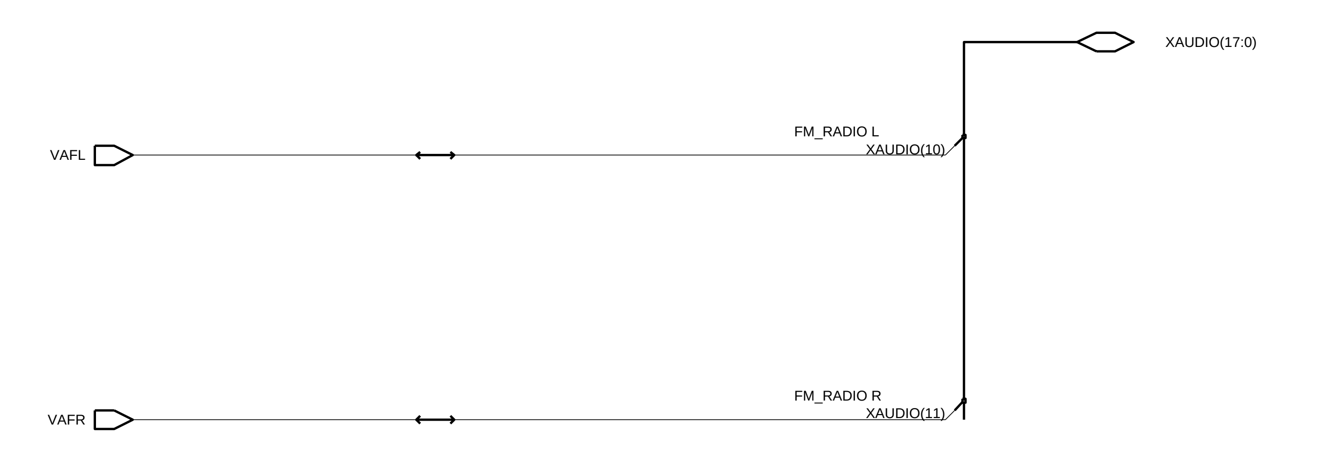
Application Engine Interface v. 1.3 ed.2



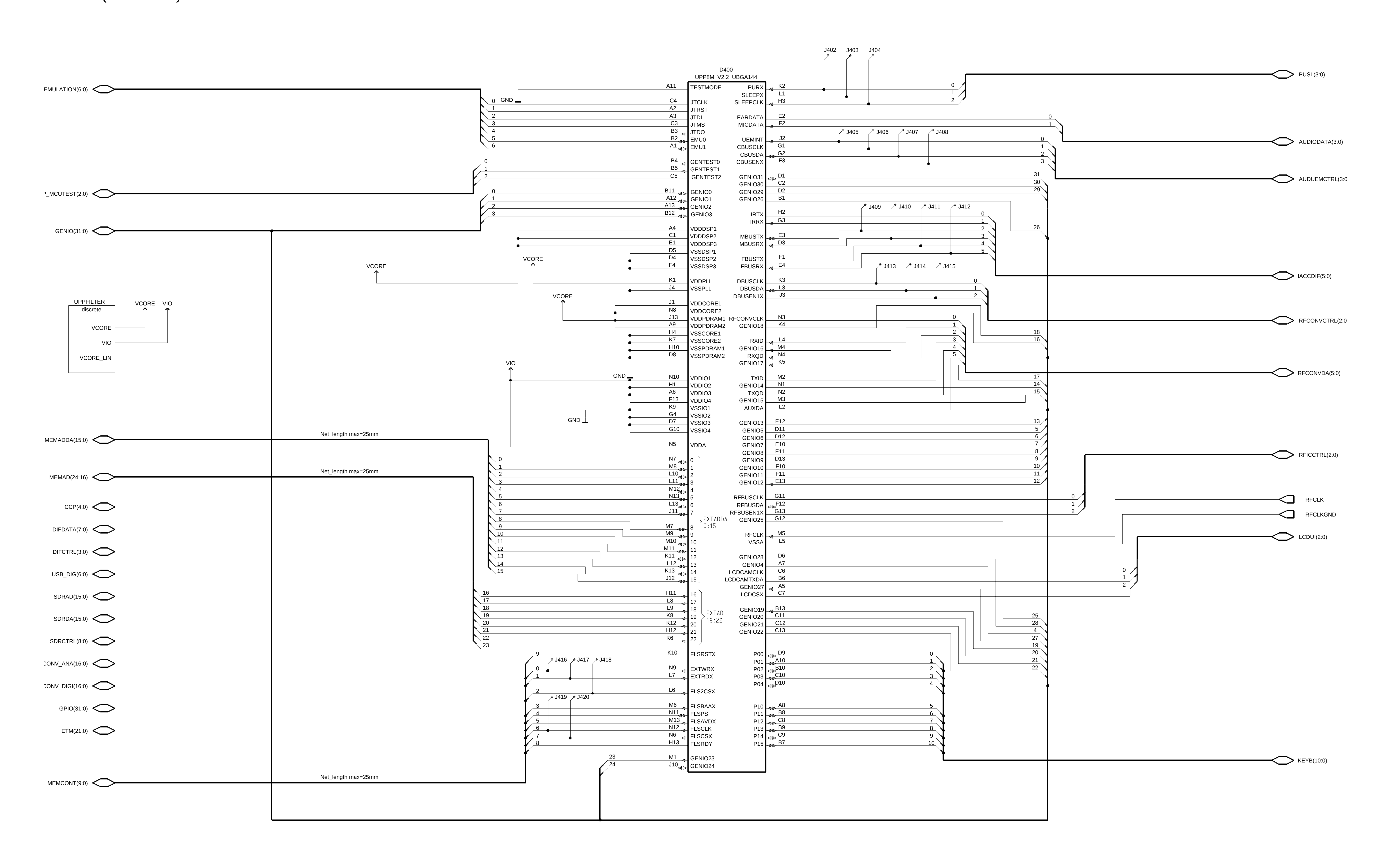
FM radio unit (v.1.3 ed.153)



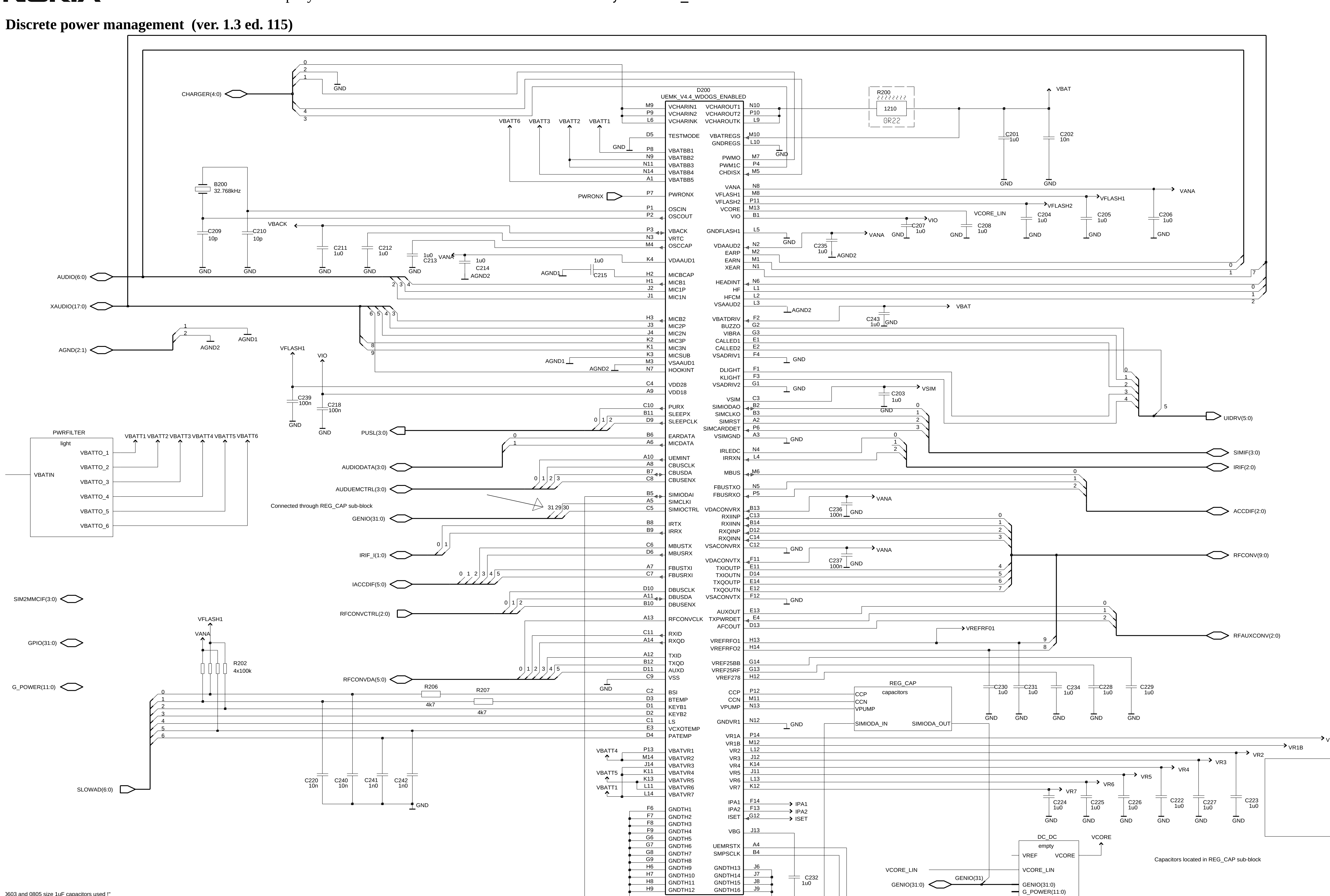
FM radio unit 2 (v. 0.0 ed.118)



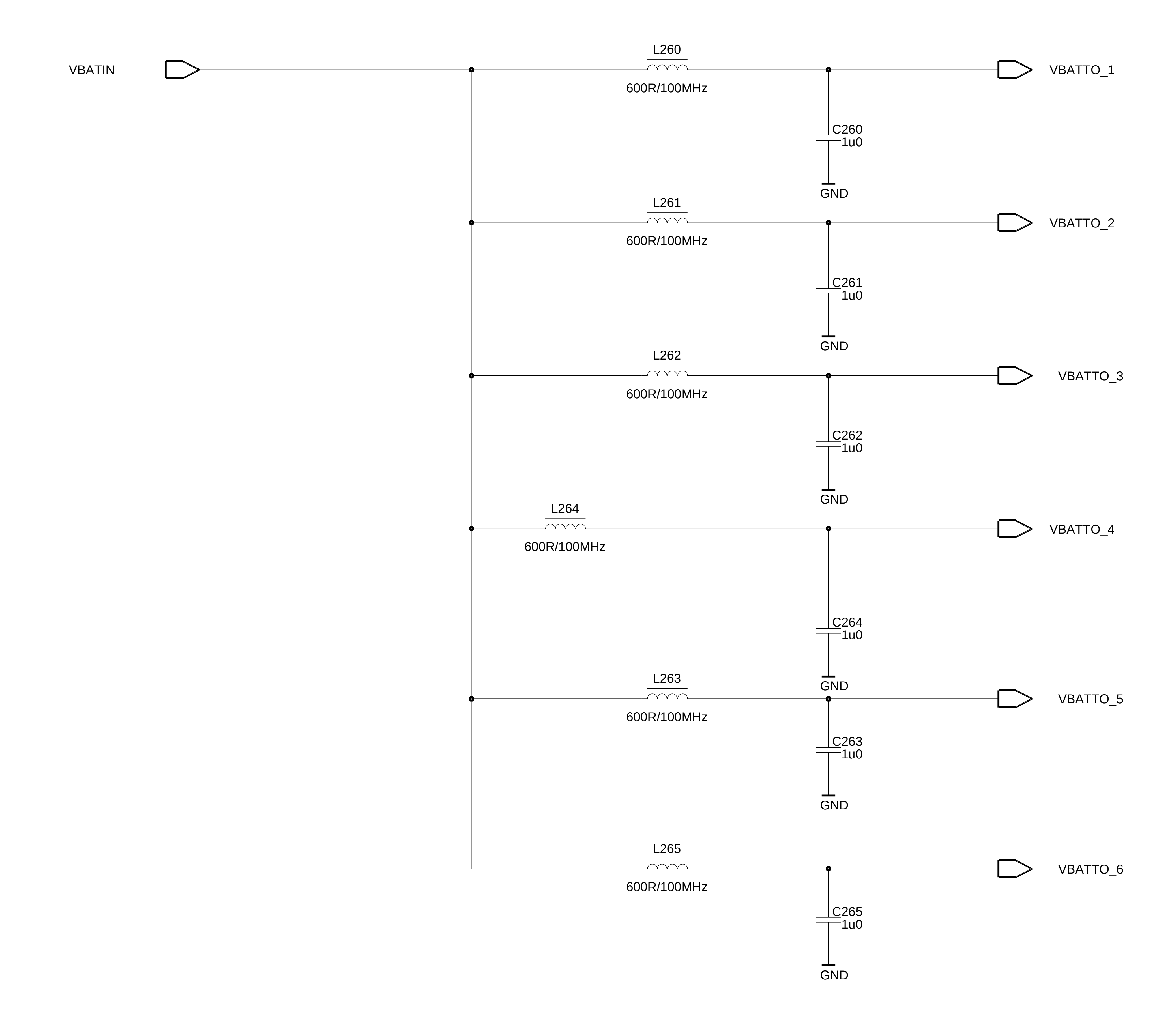
UPP 8M (v.2.0 ed.102)



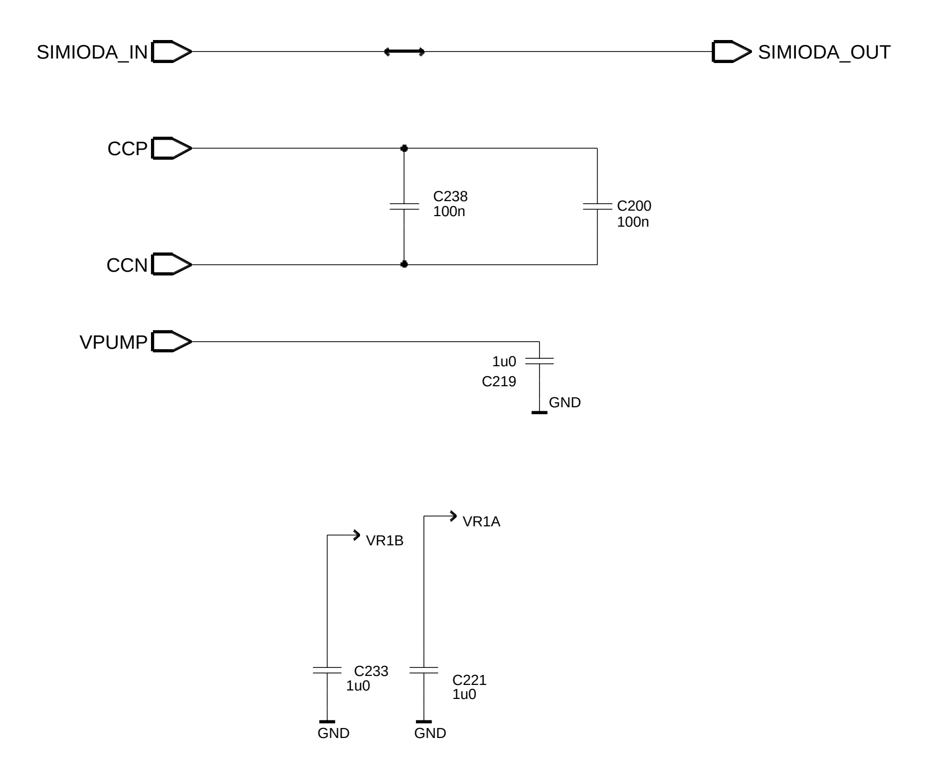
Discrete power management (ver. 1.3 ed. 115)



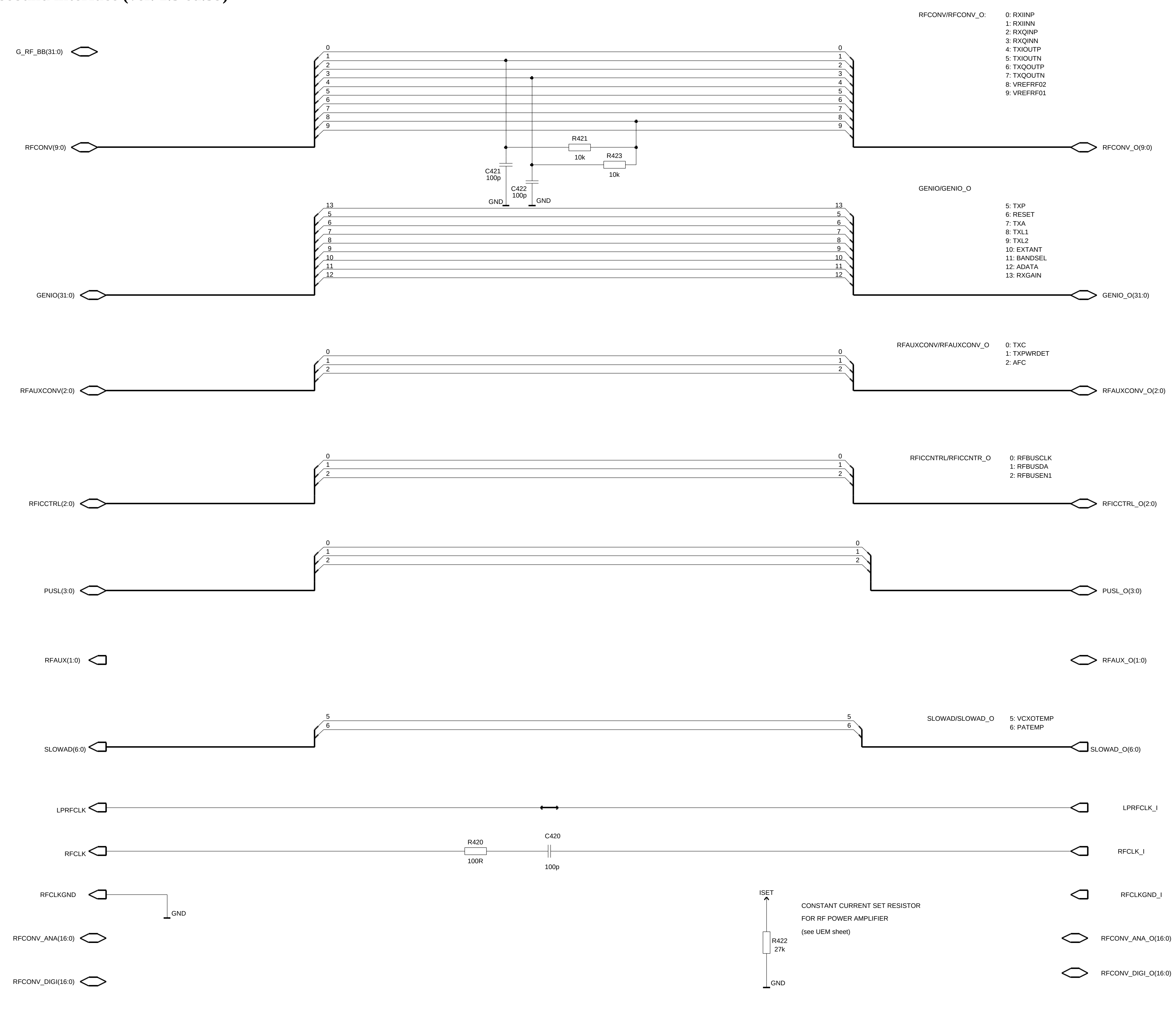
Light filtering (ver. 2.0 ed. 28)



Old power discrete (ver. 0.0 ed. 8)

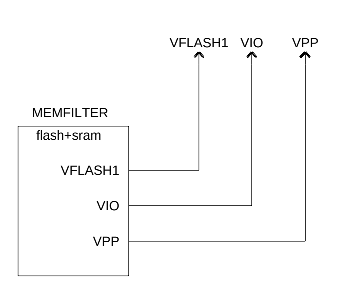


GSM RF-Baseband interface (ver. 1.3 ed.39)



IF IPA1 AND IPA2 ARE USED BY RF THE TOLERANCE HAS TO BE 1% (0402, 1430873)

Flash memory (ver. 2.0 ed.56)



MEMEXTENSION
sram4mbit
MEMADDA(15:0)
MEMAD(24:16)
MEMCONT(9:0)
GENIO(31:0)
G_MEMORY(3:0)
FLS2CSX

MEMWING
empty
GENIO(31:0)
G_MEMORY(3:0)
MEMAD(24:16)
MEMADDA(15:0) FLS2CSX
MEMCONT(9:0) FLSCSX

GENIO(31:0)
MEMAD(24:16)
MEMCONT(9:0)
MEMADDA(15:0)
G_MEMORY(3:0)

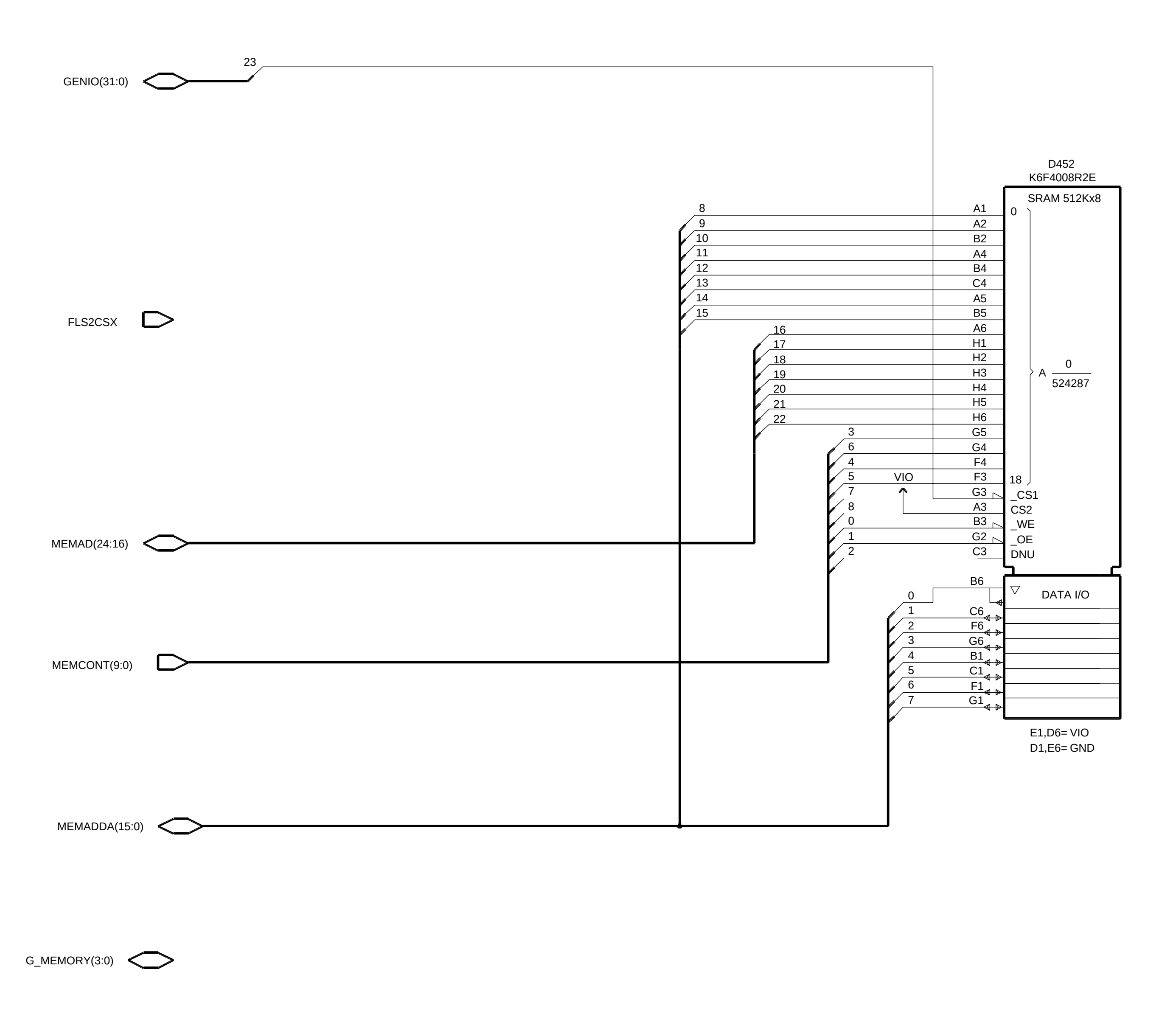
D450
NMP_DCT4_128Mbit_54MHz
FLASHM 8Mx16
A 0 8388607
CLK
_AVD
_CE
_RST/_RP
_OE
WE
_WP
RDY
NC
ADDRESS/DATA I/O

E451 E452 E453 E454 E455 E456 E457 E458

VPP PRODTP6
R450 4k7
E450

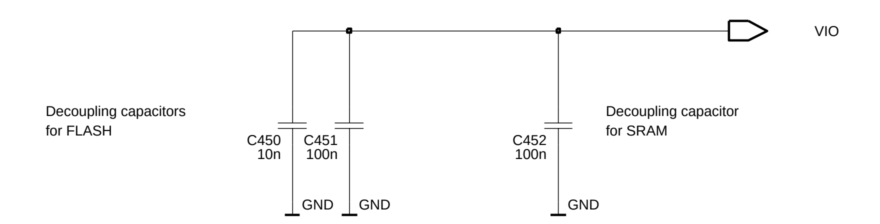
A5= VIO
B1,D8= VIO
A7= VPP
A3,C1= GND
B10,D3= GND

4Mbit SRAM (ver. 2.0 ed.31)



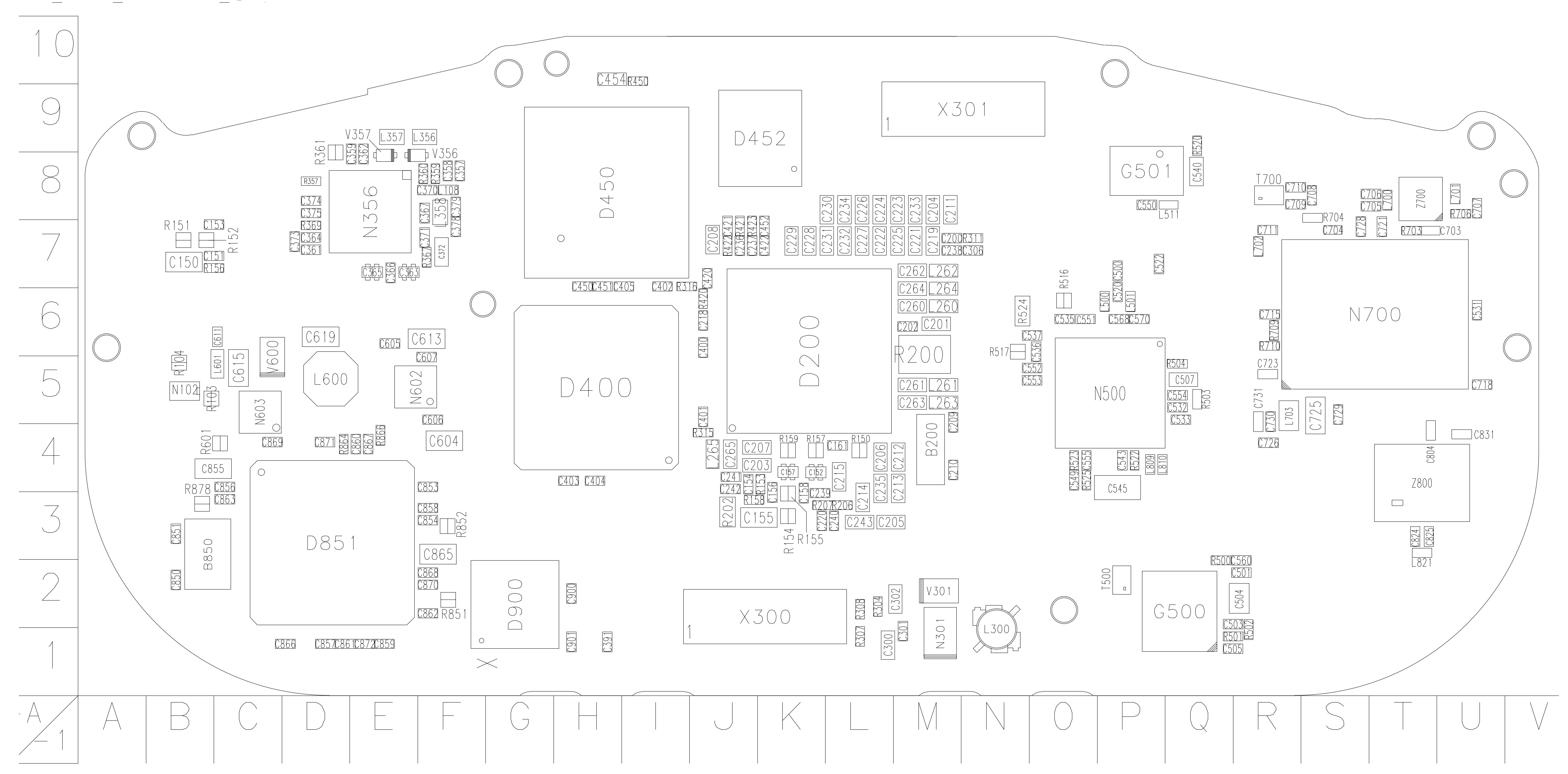
Decoupling capacitors for Flash and SRAMs (v.2.0 ed.6)

VFLASH1



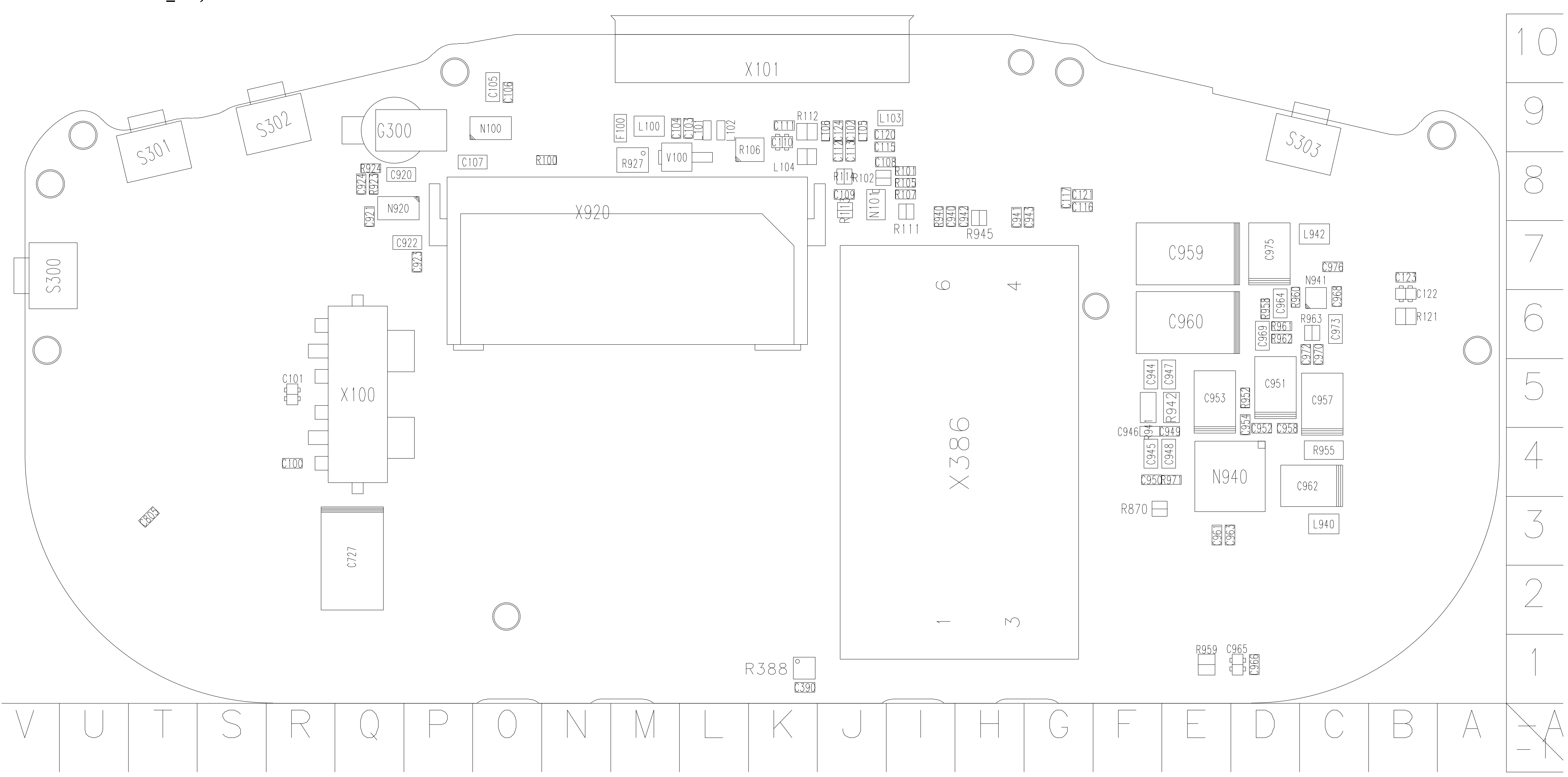


Parts Placement EM11_10a, top

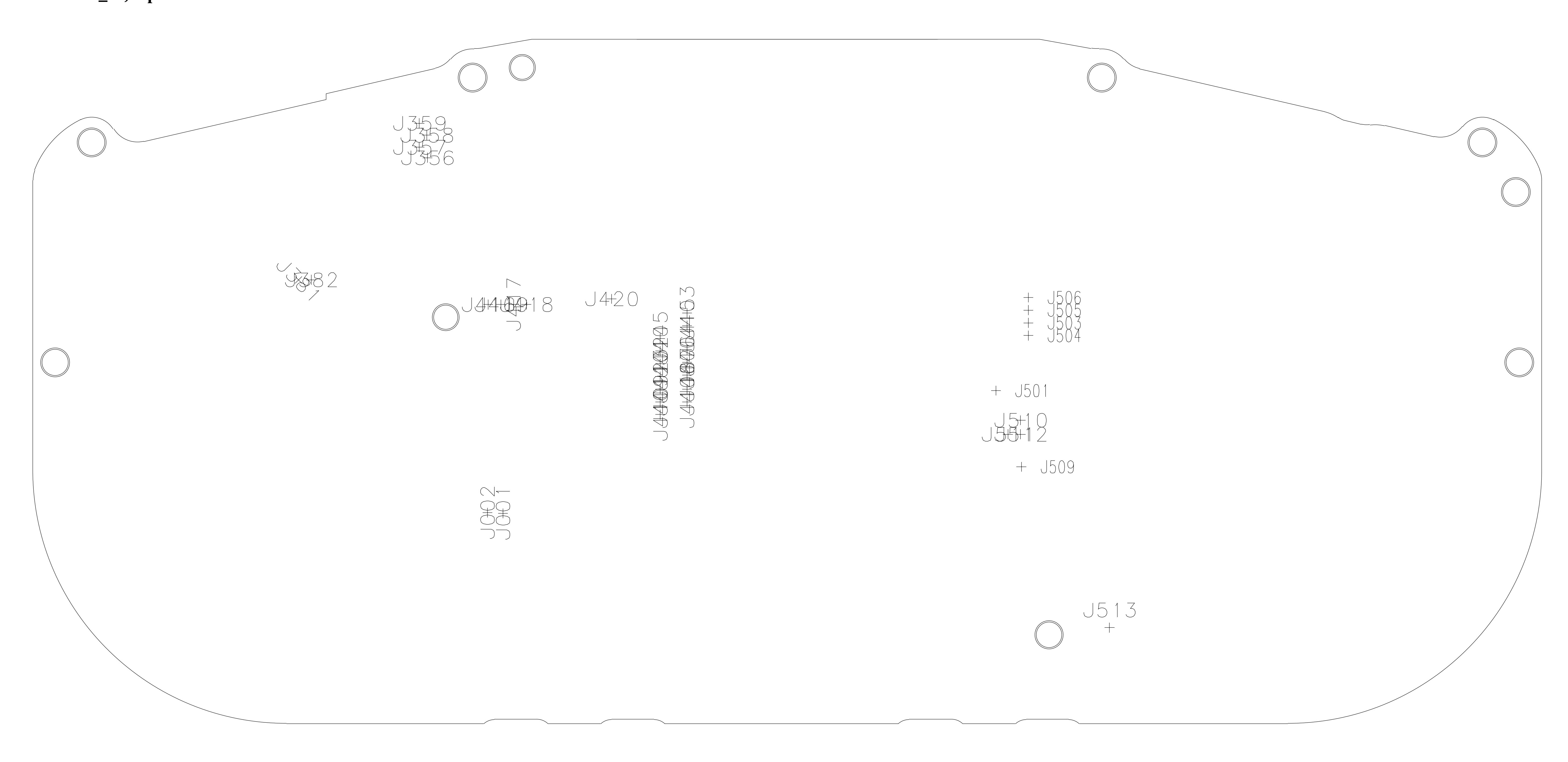


Test Points

Parts Placement EM11_10a, bottom



Test Points EM11_10, top



Test Points EM11_10, bottom

